

FIG. 1
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The diagram illustrates a computer system architecture with a central bus (61) connecting various components:

- Left Side Components:**
 - CPU (62):** The central processing unit.
 - RAM (63):** Random Access Memory.
 - ROM (64):** Read-Only Memory.
 - I/O ADAPTER (65):** Manages data flow between the bus and external devices. It is connected to a **Printer (70)** and **Disk Drives (71)**.
 - COMMUNICATION ADAPTER (66):** Facilitates network or external communication, indicated by a bidirectional arrow (72).
- Right Side Components:**
 - DISPLAY ADAPTER (68):** Controls the output to the **Monitor (75)**.
 - USER INTERFACE ADAPTER (67):** Manages input from the **Keyboard (73)** and **Mouse (74)**.

Each component is connected to the central bus (61) via a dedicated line, with reference numerals 62 through 68 indicating the connection points.

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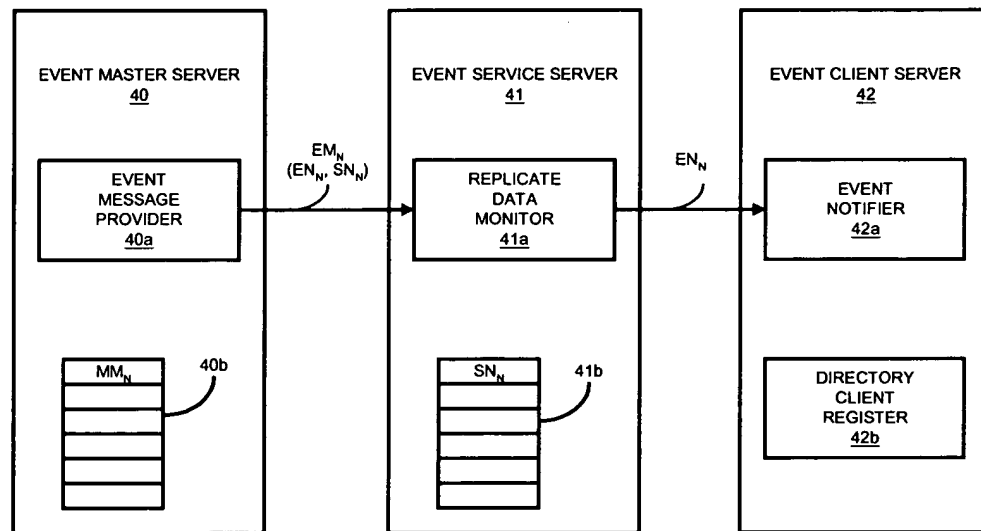


FIG. 2

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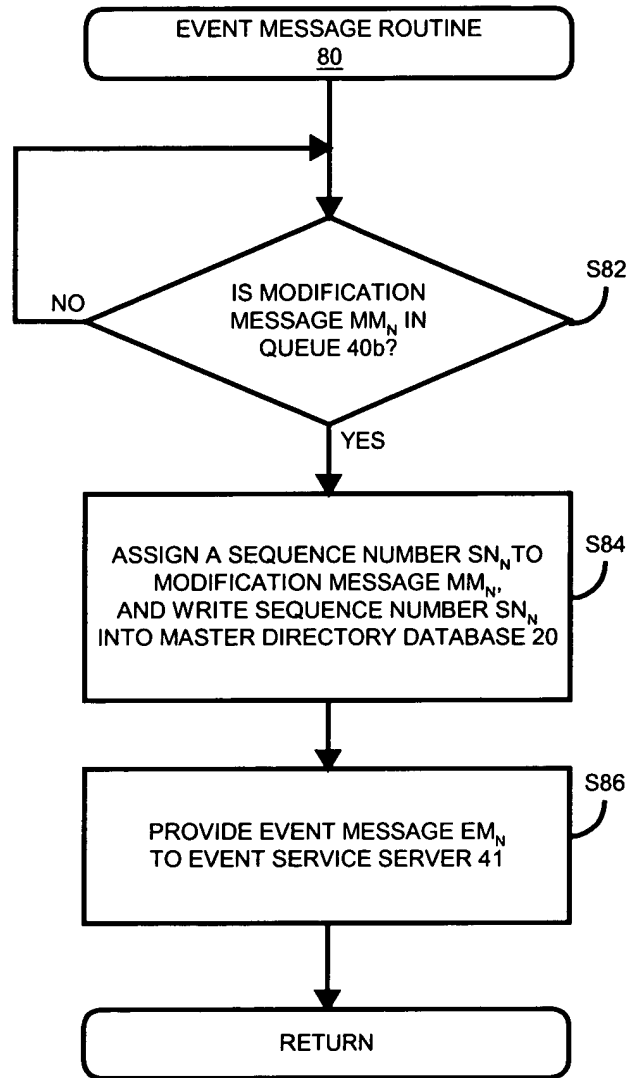


FIG. 3A
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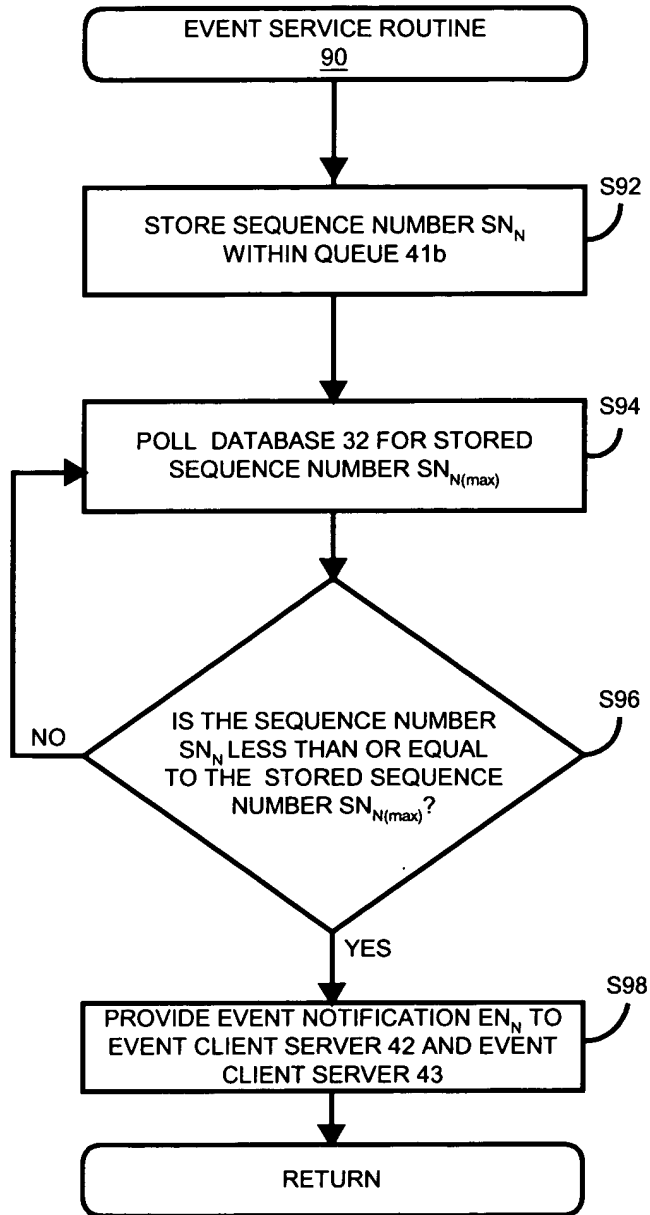


FIG. 3B
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